Project One

1.0 – Quartus Summary Report (First)

|  |  |  |  |
| --- | --- | --- | --- |
| Logical Elements | Registers | Total Pins | Memory Bits |
| 3324 | 1180 | 5 | 604672 |

2.0 – Quartus Summary Report (NIOS II/e)

|  |  |  |  |
| --- | --- | --- | --- |
| Logical Elements | Registers | Total Pins | Memory Bits |
| 1674 | 874 | 5 | 535552 |

Figure 1 - shows our number of logic elements, registers, pins, and memory bits for NIOS II/e

A screenshot of a computer

Description automatically generated

3.0 – Quartus Summary Report (NIOS II/f)

|  |  |  |  |
| --- | --- | --- | --- |
| Logical Elements | Registers | Total Pins | Memory Bits |
| 3324 | 1180 | 5 | 604672 |

Figure 2 - shows our number of logic elements, registers, pins, and memory bits for NIOS II/f

A screenshot of a computer

Description automatically generated

4.0 –Hello World!

Figure 3 – Displays our output of Hello World. This was shown after compiling and running the project.

